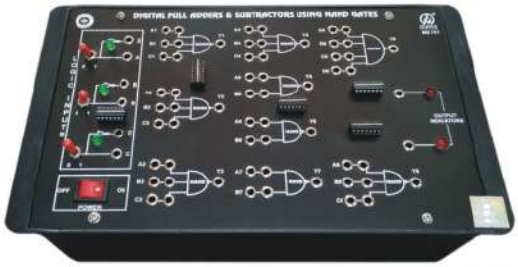
Digital Electronics

ME 701 **Adders and Subtractors using NAND gates**

Objective : The instrument has been designed to verify the Truth Tables of half adder, half subtractor, full adder & full subtractor.



Provided with NAND gates (2 Input, 3 Input & 4 Input) & housed in ABS cabinet, circuit diagram printed, connections of various gates, logic inputs & test points brought out at Glass epoxy (PCB) front panel. Also provided with patch chords & instruction manual.

ME 702 **Flip Flops using NAND gates & TTL IC's**

Objective : The instrument has been designed to verify the truth Tables of "RS" & "D" Type Flip Flops using NAND Gates, "D" Type, "T" Type, "JK" & "JK Master slave" Flip Flop using TTL Ic's.

Technical Specification :

Output voltage (Regulated) : 5V DC (Internally connected)
Housed in ABS cabinet, circuit diagram printed, connections of IC,s (7400, 7410, 7474, 7476), logic inputs, output indicators, Monoshot clock 1Hz & test points brought out at Glass epoxy (PCB) front panel. Also provided with patch chords & instruction manual.

ME 702A **RS & D type Flip Flops using TTL IC's**
ME 702B **RS & D type Flip Flops using NAND Gates**
ME 702C **JK & T Type Flip Flops using TTL IC's**
ME 702D **JK Master slave Flip Flops using TTL IC's**

ME 711 **Decade/ Modulo 'N' Counters**

Objective : The instrument has been designed to study Forward, Reverse & Modulo Counter.

Technical Specification :

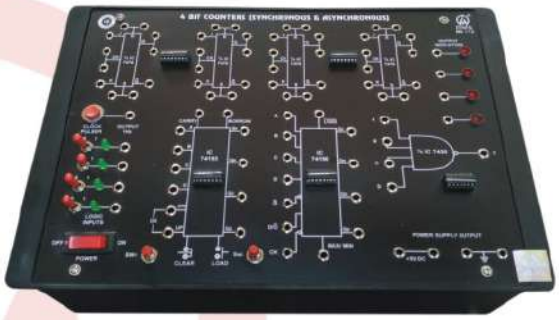
Output voltage (Regulated) : 5V DC (Internally connected)
Housed in ABS cabinet, circuit diagram printed, connections of IC 74190, logic inputs, output indicators, Monoshot clock 1Hz & test points brought out at suitable front panel.
Also provided with patch chords & instruction manual.

ME 712 **4 Bit counters (Synchronous & Asynchronous)**

Objective : The Instrument has been designed to study following :
Asynchronous counters : Forward, Reverse & Modulo
Synchronous counters : Forward, Reverse, Programmable, Decade, Ring & Jhonson Counters

Technical Specification :

Output voltage (Regulated) : 5V DC (Internally connected)
Housed in ABS cabinet, circuit diagram printed, connections of IC's (74190, 74193, 7420, 7476 -2Nos), logic inputs, output indicators, monoshot clock 1Hz & test points brought out at Glass epoxy (PCB) front panel. Also provided with patch chords & instruction manual.



ME 712A **4 Bit counters (Synchronous)**

Objective : The Instrument has been designed to study Forward, Reverse, Programmable, Decade, Ring & Jhonson Counters

Technical Specification :

Output voltage (Regulated) : 5V DC (Internally connected)
Housed in ABS cabinet, circuit diagram printed, connections of IC's (7420, 7476 -2Nos), logic inputs, output indicators, Monoshot clock 1Hz & test points brought out at Glass epoxy (PCB) front panel.
Also provided with patch chords & instruction manual.

ME 716 **4-Bit Shift Registers**

Objective : The instrument has been designed to study :

A. Universal Shift Register using IC 74194
B. Serial in, Parallel out Shift Register using IC 7495
C. Parallel in, Parallel out Shift Register using IC 7495
D. Parallel in, Serial out Shift Register using IC 7494
E. Serial in, Serial out Shift Register using IC 7494

Technical Specification :

Output voltage (Regulated) : 5V DC (Internally connected)
Housed in ABS cabinet, circuit diagram printed, connections of Ic's (74194, 7495 & 7494), logic inputs, output indicators & test points brought out at Glass epoxy (PCB) front panel.
Also provided with patch chords & instruction manual.

ME 716A **Universal shift registers using IC 74194**
ME 716B **Shift registers using IC 7495 (SIPO & PIPO Mode)**
ME 716C **Shift registers using IC 7494 (PISO & SISO Mode)**